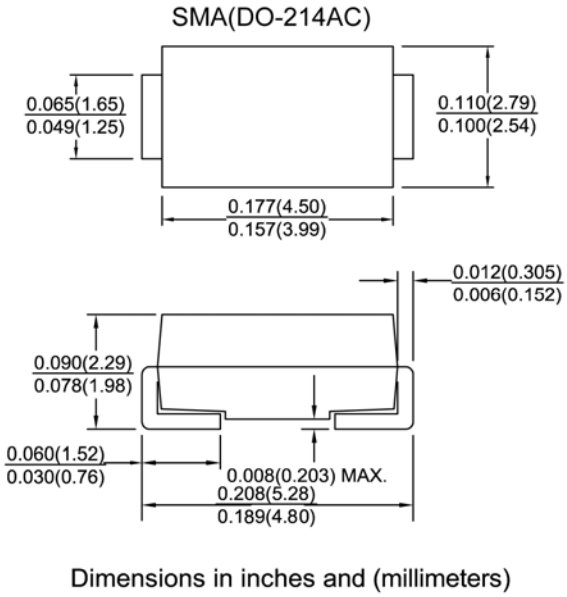
Central Plate
Electronics Co., Ltd.

SURFACE MOUNT FAST RECOVERY RECTIFIER
RS2A THRU RS2M 50 to 1000 V 2 A

FEATURES

- For surface mounted applications
- Low profile package
- Built-in strain relief
- Easy pick and place
- Fast Recovery times for high efficiency
- Plastic package has Underwriters Laboratory
- Flammability Classification 94V-O
- High temperature soldering : 260°C /10 seconds at terminals



Dimensions in inches and (millimeters)

MECHANICAL DATA

- Case: Molded plastic, DO-214AC(SMA)
- Terminals: Solder plated, solderable per MIL-STD-750,method 2026 guaranteed
- Polarity: Color band denotes cathode end
- Packaging: 12mm tape per EIA STD RS-481
- Weight: 0.003 ounce, 0.064 gram

Maximum Ratings and Electrical Characteristics

Rating at 25℃ ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, Resistive or inductive load. For capacitive load, derate current by 20%

Type Number	Symbols	RS2A	RS2B	RS2D	RS2G	RS2J	RS2K	RS2M	Units
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	Volts
Maximum RMS Voltage	V_{RMS}	35	70	140	280	420	560	700	Volts
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	Volts
Maximum Average Forward Rectified Current. 375" (9.5mm) Lead Length @ T_A =55 ℃	$I_{(AV)}$	2.0							Amp
Peak Forward Surge Current, 8.3ms single half-sine-wave superimposed on rated load (JEDEC method)	I_{FSM}	50							Amp
Maximum instantaneous Forward Voltage at @2.0A	V_F	1.3							Volts
Maximum Reverse Current @ T_A=25℃ at Rated DC Blocking Voltage @ T_A=125℃	I_R	5.0 200							uAmp
Maximum Reverse Recovery Time (Note 1)	TRR	150				250	500		nS
Typical Junction Capacitance (Note 2)	C_J	25							pF
Operating and Storage Temperature Range	T_J T_{STG}	-55 to +150							℃

NOTES:

1.Measured at1 M_{HZ} and applied reverse voltage of 4.0 VDC.
2.Thermal resistance from junction to lead mounted on P.C.B. with 0.3 x 0.3” (8.0 x 8.0mm) copper pad areas
3.Reverse Recovery Test Conditions：I_F=.5A，I_R=1A，I_{RR}=.25A.

RATINGS AND CHARACTERISTIC CURVES (RS2A THRU RS2M)

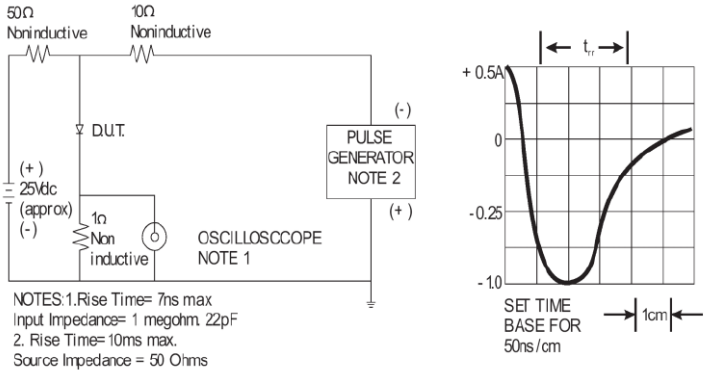


Fig.1-REVERSERECOVERY TIME CHARACTERISTIC AND TEST CIRCUITDIAGRAM

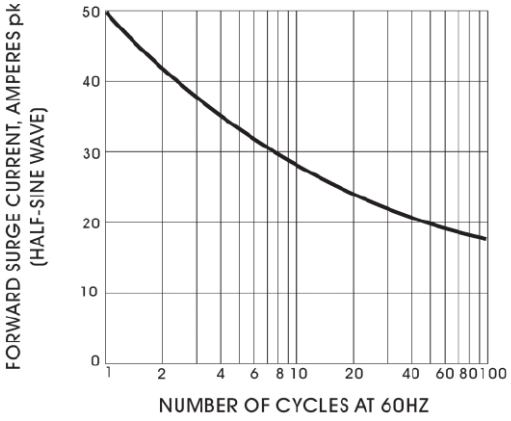


Fig.2-MAXIMUM OVERLOAD SURGE-CURRENT

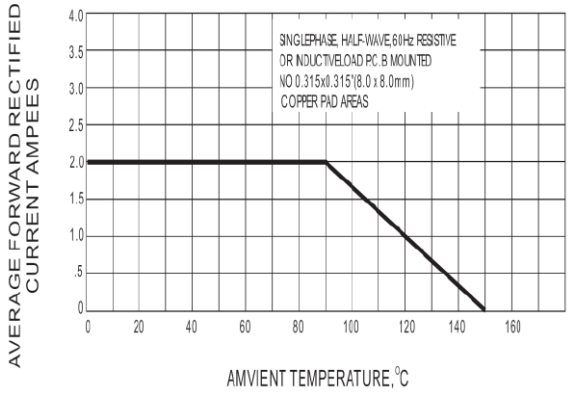


Fig. 3-MAXIMU AVERAGE FORWARD CURRENT RATING

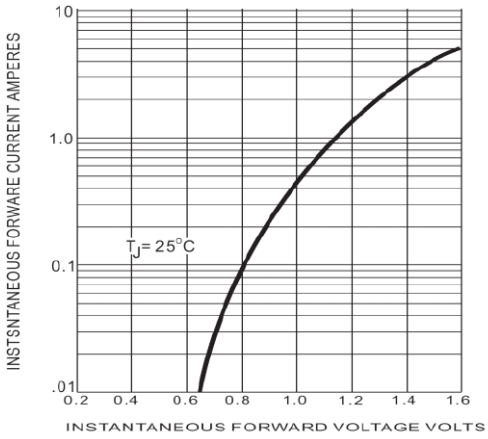


Fig. 4- FORWARD CURRRENT DERATING CUR\

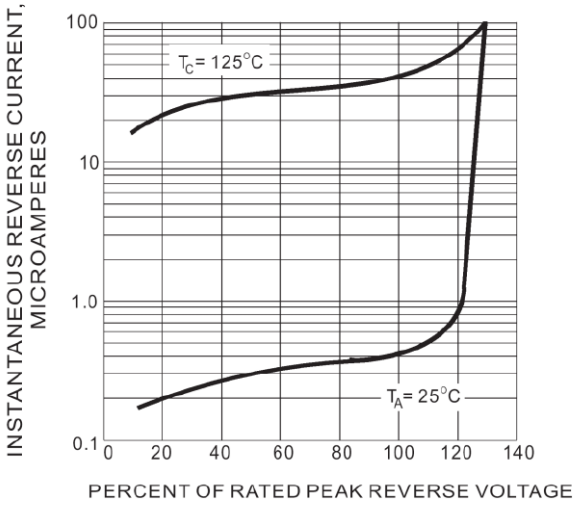


Fig. 5-TYPICAL REVERSE CHARACTERISTICS

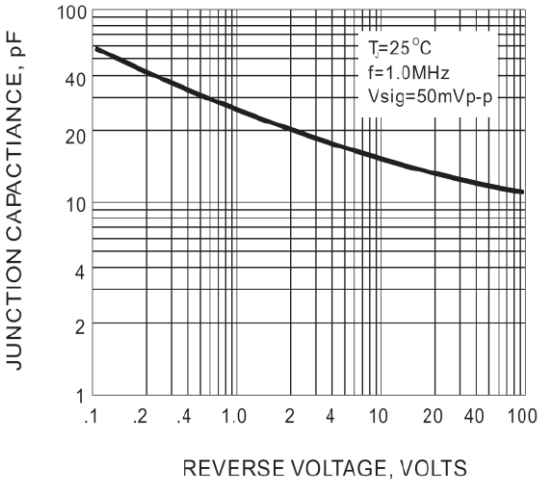


Fig.6- TYPICAL JUNCTION CAPACITANC