

SCHOTTKY BARRIER RECTIFIER SR520 THRU SR5200 20 to 200 V 5.0 A

Features

- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- Metal silicon junction ,majority carrier conduction
- Guard ring for over voltage protection
- Low power loss ,high efficiency
- High current capability ,Low forward voltage drop
- High surge capability
- For use in low voltage ,high frequency inverters, Free wheeling and polarity protection applications
- High temperature soldering guaranteed: 260°C/10 seconds at terminals,
- 0.375"(9.5mm)lead length,5lbs.(2.3kg)tension

Mechanical Data

- Case: JEDEC DO-201AD And DO-27 molded plastic body
- Terminals: solder plated ,solder able per MIL-STD-750,method 2026
- Polarity: color band denotes cathode end
- Mounting Position: Any
- Weight: 0.041ounce,1.15 gram

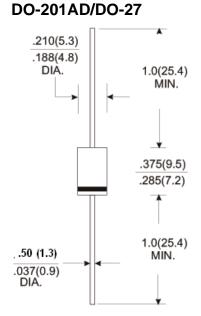
Maximum Ratings and Electrical Characteristics

Ratings at 25° C ambient temperature unless otherwise specified. Single phase, half wave, 60HZ, resistive or inductive load. For capacitive load, derate current by 20%.

	Symbols	SR 520	SR 530	SR 540	SR 550	SR 560	SR 580	SR 5100	SR 5150	SR 5200	Units
Maximum Recurrent Peak Reverse Voltage	V _{RRM}	20	30	40	50	60	80	100	150	200	Volts
Maximum RMS Voltage	V _{RMS}	14	21	28	35	42	57	71	105	140	Volts
Maximum DC Blocking Voltage	V _{DC}	20	30	40	50	60	80	100	150	200	Volts
Maximum average forward rectified current 0.375"(9.5mm) lead length (See Fig.1)	I _(AV)	5.0									Amp
Peak Forward Surge Current, 8.3ms single											
half-sine-wave superimposed on rated load	I _{FSM} 150.0									Amp	
(JEDEC method)											
Maximum instantaneous forward voltage at5.0 A(Note 1)	V _F	0.55			0.	70	0.80		0.85	0.95	Volts
Maximum Reverse Current T₄=25℃		0.5									mAmp
at Rated DC Blocking Voltage $T_A=100^{\circ}C$	I _R	20									
Maximum Reverse Recovery Time (Note 4)	T _{RR}	15									nS
Typical junction capacitance(Note 3)	CJ	340 320							₽F		
Typical Thormal Pagistones (Note 2.)	R_{θ} JA	25.0									°C/W
Typical Thermal Resistance (Note 2)	R_{θ} JL	8.0									
Operating Temperature Range	TJ	-65 to +150									°C
Storage Temperature Range	T _{STG}	-65 to +150									°C

NOTES:

- 1. Pulse test: 300 μ s pulse width, 1% duty cycle
- 2. Thermal resistance from junction to lead vertical P.C.B. mounted, 0.375"(9.5mm) lead length
- 3. Measured at 1.0MHz and reverse voltage of 4.0 volts
- 4. Reverse Recovery Test Conditions: IF =0.5A, IR =1.0A, IRR =0.25A



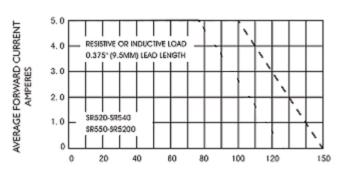
Dimensions in inches and (millimeters)



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RATINGS AND CHARACTERISTIC CURVES (SR520 THRU SR5200)

FIG.1-FORWARD CURRENT DERATING CURVE



LEAD TEMPERATURE (C)



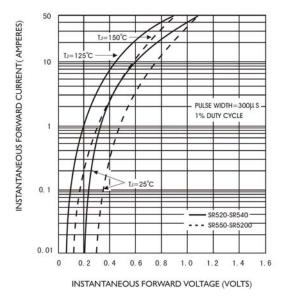


FIG.5-TYPICAL JUNCTION CAPACITANCE

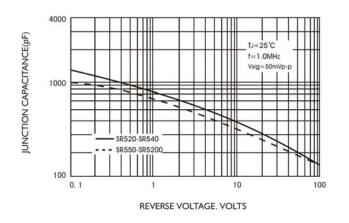
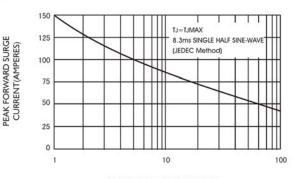


FIG.2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT



NUMBER OF CYCLES AT 60Hz

FIG.4-TYPICAL REVERSE CHARACTERISTICS

